

**REMARKS/ARGUMENTS**

In view of the amendments and remarks herein, favorable reconsideration and allowance of this application are respectfully requested. By this Amendment, Claims 2 and 9 have been canceled and claims 1, 3, 10, 11, 12 and 18-22 have been amended. Thus, claims 1, 3-8 and 10-23 are pending for further examination.

Claims 1-10, 12, 15-16, 18, 21 and 23 have been rejected under 35 USC 102(e) as being anticipated by Chen. In addition, claims 11, 13, 14, 17, 19, 20 and 22 have been rejected under 35 USC 103 as being obvious over Chen in view of Nally and Nakamura.

For at least the following reasons, Applicant respectfully submits that amended claim 1 and the dependent claims thereon are not anticipated by or rendered obvious by the cited references. Thus, reconsideration and withdrawal of this rejection are respectfully requested.

The Examiner has indicated that the previous amendments and arguments were not persuasive because, at least in part, the claim language “an external memory location” was interpreted by the Examiner to only require that the copying be outside the frame buffer and not necessarily outside the chip containing the frame buffer. As a result, the Examiner indicated that the display memory chip 18 and the texture memory 124 of Chen met this claim limitation. In response, Applicant has amended independent claim 1 herein to expressly require that the copy pipeline transfers data from the embedded frame buffer to a main memory of the graphics system which resides on a chip that is separate from the graphics chip. This amendment eliminates the Examiner’s interpretation of the

prior claim language and distinguishes the display memory chip (which does not contain main memory of the graphics system) and the texture memory (which is on the same chip as the frame buffer in the M chips identified by the examiner). In view of this amendment, Applicant believes that the outstanding rejections and reasoning therefore provided in the outstanding Office Action are now moot. Thus, reconsideration and withdrawal of the rejections are respectfully requested.

In addition, Applicant respectfully submits that Chen does not disclose the claimed feature “wherein the copy pipeline converts the data from one format to another format after reading the data from the embedded frame buffer and during transfer of the data from the embedded frame buffer to the main memory,” as required by amended claim 1. The conversion of data identified by the Examiner in Chen is done by the rasterizer chip 16. None of the teachings of Chen cited by the Examiner teach the copy out feature of claim 1, as amended herein, in which the copy pipeline converts the data from one format to another format after reading the data from the embedded frame buffer of a graphics co-processor and during transfer of the data from the embedded frame buffer to the main memory.

For at least the above reasons, Applicant respectfully submits that amended claim 1 is not anticipated by Chen. Moreover, the remaining references of record fail to make up for the deficiencies of Chen. Thus, allowance of amended claim 1 and dependent claims thereon are respectfully requested.

Independent claim 12 has been amended to recite a method of transferring data from a graphics chip to a main memory of a graphics system that is provided on a chip separate from the graphics chip, including: storing image data in an embedded frame buffer of the graphics chip; initiating a copy out operation for transferring data from the embedded frame buffer to the main memory; converting the data from one format to another format during the copy out operation between the embedded frame buffer and the main memory; and writing the converted data to the external image storage destination. Thus, claim 12 as presently presented requires that the copy out operation transfer data from an embedded frame buffer to a main memory of the graphics system that is on a different chip from graphics chip, and that the conversion occur during the copy out operation between the embedded frame buffer and the graphics chip. Thus, Applicant respectfully submits that claim 12 and the remaining dependent claims are allowable over the prior art of record for substantially the same reasons set forth above with respect to claim 1. Thus, reconsideration and withdrawal of this rejection are respectfully requested.

In view of the foregoing amendments and remarks, Applicant respectfully submits that all of the pending claims patentably distinguish the prior art of record and are in condition for allowance. Thus, withdrawal of the rejections and passage of this case to issuance at an early date are earnestly solicited.

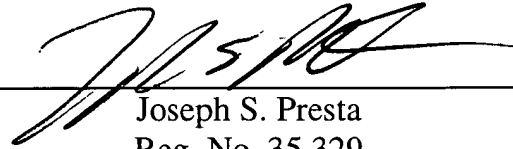
FOULADI et al.  
Appl. No. 09/722,663  
August 9, 2004

Should the Examiner have any questions regarding this response, or deem that any further issues need to be addressed prior to allowance, the Examiner is invited to call the undersigned attorney at the phone number below.

Respectfully submitted,

**NIXON & VANDERHYE P.C.**

By: \_\_\_\_\_

  
Joseph S. Presta  
Reg. No. 35,329

JSP:mg  
1100 North Glebe Road, 8th Floor  
Arlington, VA 22201-4714  
Telephone: (703) 816-4000  
Facsimile: (703) 816-4100